CLAIM LISTING

Claims 1-6 are cancelled without prejudice.

Please add the following claims:

--7.   (New) A circuit for storing data, said circuit comprising:

a FIFO for queuing the data;

a read pointer for indicating a particular address in the FIFO;

a write pointer for indicating another particular address in the FIFO; and

a comparator for determining whether the FIFO is empty or full based on a comparison of a Gray code associated with the read pointer and a Gray code associated with the write pointer.

8.   (New) The circuit of claim 7, further comprising:

a first Gray code generator for generating the Gray code associated with the read pointer; and

a second Gray code generator for generating the Gray code associated with the write pointer.

9.   (New) The circuit of claim 8, further comprising:

a first Gray code to binary converter for generating the particular address indicated by the read pointer; and

a second Gray code to binary converter for generating the another particular address indicated by the write pointer.

10.   (New) The circuit of claim 7, wherein the FIFO comprises a FIFO RAM.--